



SRINIVASA EDUCATIONAL SOCIETY'S
PACE INSTITUTE OF TECHNOLOGY & SCIENCES
(AUTONOMOUS)

Approved by AICTE and Govt. of Andhra Pradesh, Accredited by NAAC(A Grade) | Recognized under 2(f) & 12(B) of UGC
 Permanently Affiliated to JNTUK, Kakinada, A.P. | ISO 9001:2015, ISO 14001:2015 and ISO 50001:2018 Certified Institution
 NH-16, Near Valluramma Temple, ONGOLE - 523 272, A.P., India, Ph.: 08592 278315, 9581456310 | www.pace.ac.in

Lr No: PACE(A)/ECS/2025-26/EE/74

15-04-2026


CIRCULAR


All the students are hereby informed that III B.Tech II Semester (R21) Supplementary (2021,2022 Admitted batches) Examinations will commence from **29-04-2026**. The Time Table for these examinations is given below.

| Branch | Timing of the Semester End Examinations: 10:00 am to 1:00 pm | | | | |
|---------------------------|--|------------------------------------|-------------------------------------|---|---------------------------------|
| | Date & Day | | | | |
| | 29-04-2026 (Wed Day) | 01-05-2026 (Fri Day) | 04-05-2026 (Mon Day) | 06-05-2026 (Wed Day) | 08-05-2026 (Fri Day) |
| CIVIL | Design and Drawing of Steel Structures | Water Resources Engineering | Foundation Engineering | Introduction to Database Management Systems | Employability Skills |
| | | | | Environmental Impact Assessment | |
| EEE | Power system analysis | Switch gear and protection | Linear & Digital IC Applications | Utilisation Of Electrical Energy | Design thinking for Innovation |
| | | | | Power Semiconductor Drives | |
| ME | Metrology & Measurements | Heat & Mass Transfer | Design of Machine Elements -II | Renewable Sources of Energy | Constitution of India |
| | | | | Production Planning Control | |
| ECE | Microwave & Radar Engineering | Internet of Things | VLSI Design | Optical Fiber Communication | Intellectual Property Rights |
| | | | | Digital Image Processing | |
| CSE | Cryptography and Network Security | Advanced Java and Web Technologies | Automata Theory and Compiler Design | Introduction to Simulation Software | Entrepreneurship and Innovation |
| CS&IT | Cryptography and Network Security | Machine Learning | Automata Theory and Compiler Design | Management Science | Design thinking for Innovation |
| IT | OOAD Using UML | Machine Learning | Design and Analysis of Algorithms | Management Science | Design Thinking for innovation |
| CSE (IOT&CSBT) | Sensor Network Technologies | Reinforcement Learning | Design and Analysis of Algorithms | Embedded Systems | Design Thinking for innovation |
| | | | | Management Science | |
| AI&DS | Data Science | Deep Learning | Design & Analysis of Algorithms | Introduction to Simulation Software | Design Thinking For Innovation |
| AI&ML | Data Science | Deep Learning | Design & Analysis of Algorithms | Introduction to Simulation Software | Design Thinking For Innovation |
| CSE (RL) | Cryptography and Network Security | Advanced Java and Web Technologies | Automata Theory and Compiler Design | IPR&PATENTS | Entrepreneurship and Innovation |

NOTE:

- i) Any Omissions or clashes in this Time Table may please be informed to the Controller of Examinations immediately
- ii) Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual


CONTROLLER OF EXAMINATIONS
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VALLUR, ONGOLE-523 272, ANDHRA PRADESH


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| : Dean Academics - for information | : Administrative Officer- for information |
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